TOSHIBA Field Effect Transistor Silicon N-Channel MOS Type (U-MOS V-H)

TPCA8031-H

High-Efficiency DC-DC Converter Applications

Notebook PC Applications

Portable Equipment Applications

- Small footprint due to a small and thin package
- High-speed switching
- Small gate charge: QSW = 5.0 nC (typ.)
- Low drain-source ON-resistance: RDS (ON) = 7.3 m Ω (typ.)
- High forward transfer admittance: $|Y_{fs}| = 60 \text{ S (typ.)}$
- Low leakage current: $I_{DSS} = 10 \mu A (max) (V_{DS} = 30 V)$
- Enhancement mode: $V_{th} = 1.5 \text{ to } 2.5 \text{ V } (V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA})$

Absolute Maximum Ratings (Ta = 25°C)

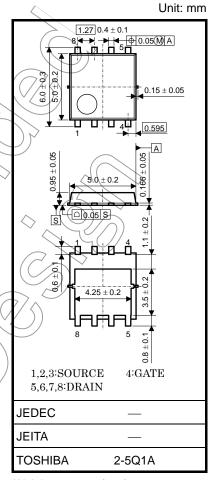
Characteristic		Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	30	V	
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)		V _{DGR}	30	V	
Gate-source voltage		V _{GSS}	±20	<\\v	
Drain current	DC (Note 1)	I _D ((24	A	
	Pulsed (Note 1)	TDB	72	,	
Drain power dissipation (Tc=25°C)		(PD	30	/ w	
Drain power dissipation (t = 10 s) (Note 2a)		PD	2.8	\$	
Drain power dissipation (t=10 s) (Note 2b)		PD	(1.6/)	W	
Single-pulse avalanche energy (Note 3)		EAS	75	mJ	
Avalanche current		I _{AR}	24	Α	
Repetitive avalanche energy (Tc = 25°C) (Note 4)		EAR	3.0	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	-55 to 150	°C	

Note: For Notes 1 to 4, refer to the next page.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the

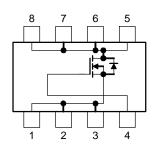
reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

This transistor is an electrostatic-sensitive device. Handle with care.



Weight: 0.069 g (typ.)

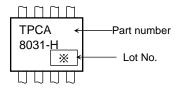
Circuit Configuration



Thermal Characteristics

Characteristic	Symbol	Max	Unit
Thermal resistance, channel to case $(\mbox{Tc} = 25\mbox{°C}) \label{eq:Tc}$	R _{th (ch-c)}	4.17	°C/W
Thermal resistance, channel to ambient $(t=10 \; s) \eqno(Note \; 2a)$	R _{th (ch-a)}	44.6	°C/W
Thermal resistance, channel to ambient (t = 10 s) (Note 2b)	R _{th (ch-a)}	78.1	°C/W

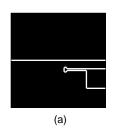
Marking (Note 5)



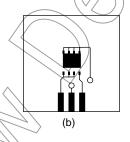
Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: (a) Device mounted on a glass-epoxy board (a)

(b) Device mounted on a glass-epoxy board (b)



25.4 × 25.4 × 0.8 (Unit: mm)



 $\begin{aligned} & \text{FR-4} \\ 25.4 \times 25.4 \times 0.8 \\ & \text{(Unit: mm)} \end{aligned}$

Note 3: $V_{DD} = 24 \text{ V}$, $T_{gh} = 25^{\circ}\text{C}$ (initial), $L = 100 \mu \text{ H}$, $R_G \neq 25 \Omega$, $I_{AR} = 24 \text{ A}$

Note 4: Repetitive rating: pulse width limited by maximum channel temperature

Note 5: * Weekly code: (Three digits)

Week of manufacture

(01) for the first week of the year, continuing up to 52 or 53)

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Year of manufacture

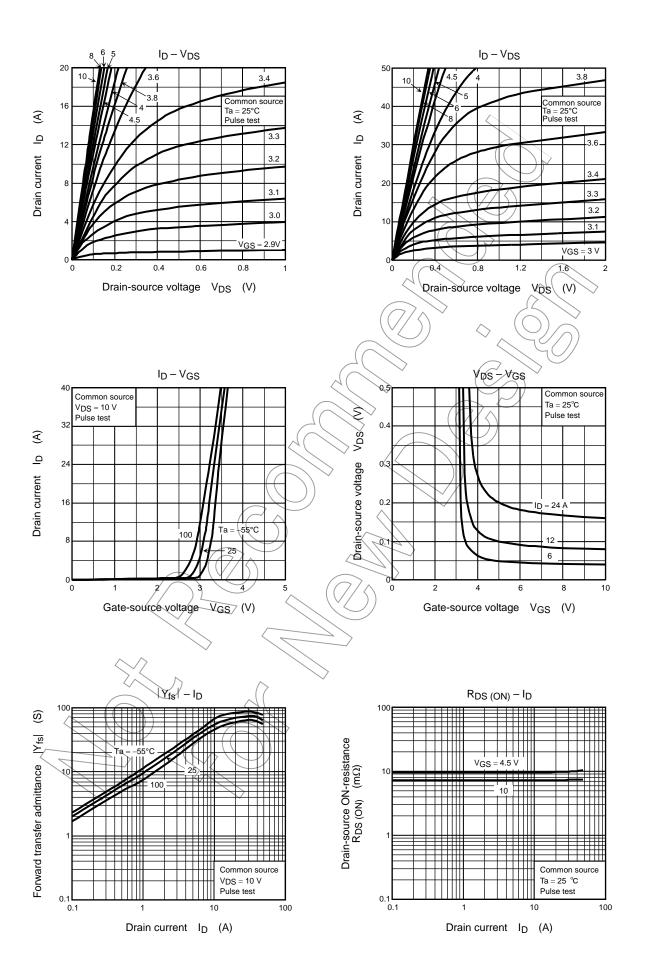
(The last digit of the year)

Electrical Characteristics (Ta = 25°C)

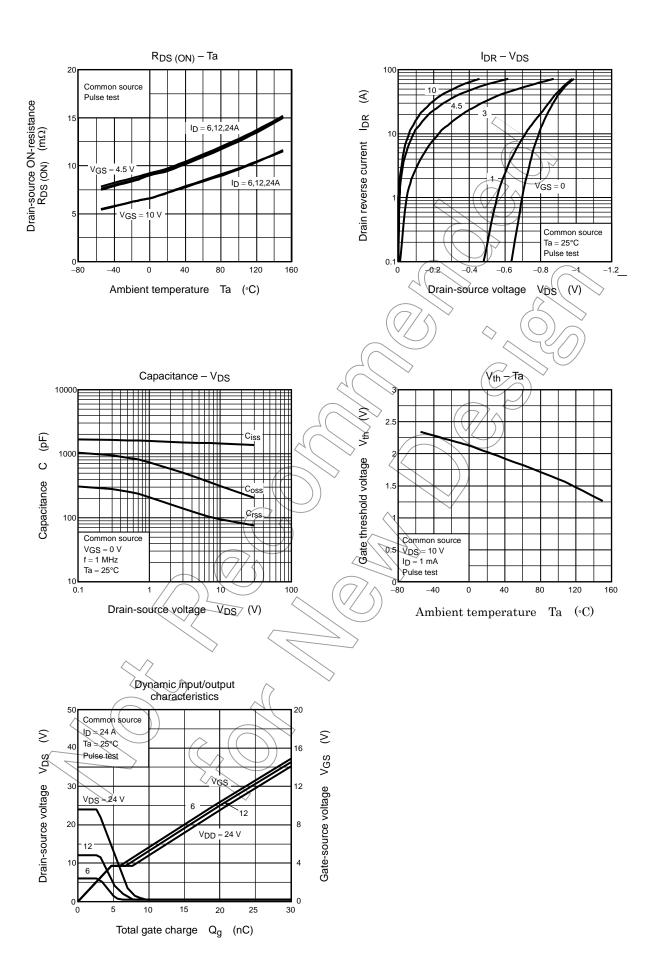
Ch	aracteristic	Symbol	Test Condition	Min	Тур.	Max	Unit	
Gate leakage cur	rent	I _{GSS}	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±100	nA	
Drain cutoff curre	nt	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V		_	10	μΑ	
Drain-source breakdown voltage		V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	30	_	_	V	
		V (BR) DSX	$I_D = 10 \text{ mA}, V_{GS} = -20 \text{ V}$	15	_	_		
Gate threshold vo	oltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	1.5) >_	2.5	V	
Drain-source ON-resistance		_	V _{GS} = 4.5 V, I _D = 12 A	<u> </u>	9.6 13.4	13.4	- mΩ	
		R _{DS} (ON)	V _{GS} = 10 V, I _D = 12 A))	7.3	11.0		
Forward transfer	admittance	Y _{fs}	V _{DS} = 10 V, I _D = 12 A	30	60	_	S	
Input capacitance	9	C _{iss}		_	1433	2150		
Reverse transfer	capacitance	C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	83	125	pF	
Output capacitan	се	C _{oss}			303	\nearrow		
Gate resistance		Rg	V _{DS} = 10 V, V _{GS} =0 V, f = 5 MHz	-	3.4	> 5.1	Ω	
Switching time	Rise time	t _r	V _{GS} 10 V I _D = 12 A V _{OUT} G		3.4) —		
	Turn-on time	t _{on}		7	9.6	_	nc	
	Fall time	t _f	1.25g		9.9	_	ns ns	
	Turn-off time	t _{off}	Duty ≤ 1%, t _w ≠ 10 μs	_	33	_		
Total gate charge (gate-source plus gate-drain)		Qg	$V_{DD} \approx 24 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 24 \text{ A}$	_	21	_		
			$V_{DD} \approx 24 \text{ V}, V_{GS} = 5 \text{ V}, V_{D} = 24 \text{ A}$		11			
Gate-source char	ge 1 /	Q _{ĝs1}		_	4.7		nC	
Gate-drain ("Miller") charge		Q_{gd}	$V_{DD} \approx 24 V_{\bullet} V_{GS} = 10 \text{ V}, I_D = 24 \text{ A}$	_	3.0	_		
Gate switch charg	ge	Q _{SW}		_	5.0	_		

Source-Drain Ratings and Characteristics (Ta = 25°C)

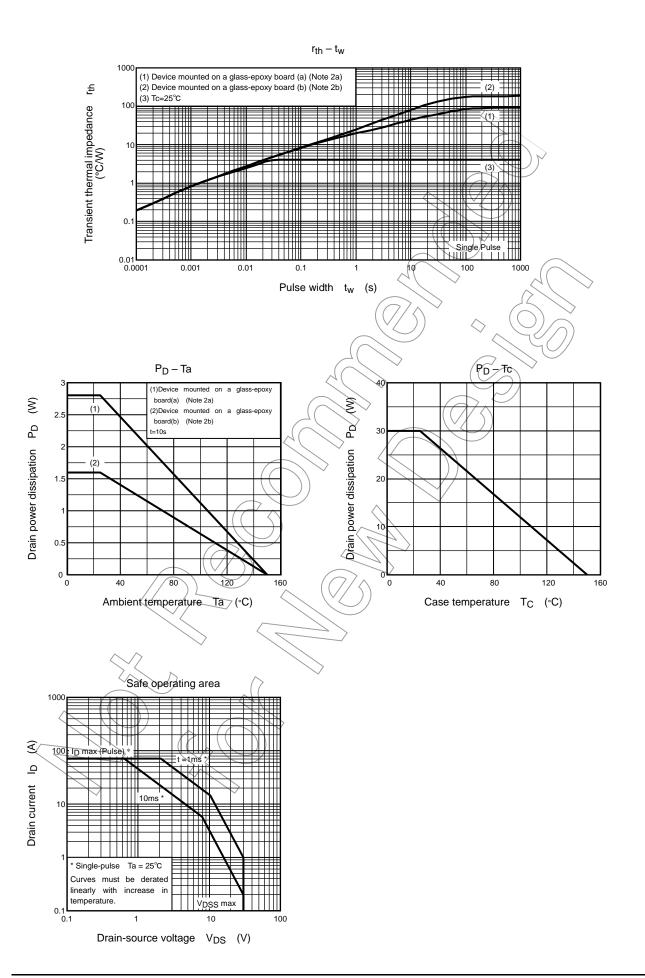
Characteristic	Symbol Test Condition	Min	Тур.	Max	Unit
Drain reverse current Pulse (Note 1)	I _{DRP} —	_	_	72	Α
Forward voltage (diode)	V_{DSF} $I_{DR} = 24 \text{ A}, V_{GS} = 0 \text{ V}$	_	_	-1.2	V



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